

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

<b>Applicant(s):</b>	Eric A. Johnson et al.	<b>Examiner:</b>	Unassigned
<b>Serial No:</b>	To be assigned	<b>Art Unit:</b>	Unassigned
<b>Filed:</b>	Herewith	<b>Docket:</b>	END920010133US1 (15171)
<b>For:</b>	PASSIVE ALIGNMENT OF VSCELS TO WAVEGUIDES IN OPTO-ELECTRONIC CARDS AND PRINTED CIRCUIT BOARDS		
<b>Dated:</b>	10/24/2003		

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**

Sir:

In accordance with 37 C.F.R. §§ 1.97 and 1.98, it is requested that the following references, which are also listed on the attached Form PTO-1449, be made of record in the above-identified case.

1. U.S. Patent No. 6,222,967 B1, dated April 24, 2001, issued to Amano et al;
2. U.S. Patent No. 6,164,837, dated December 26, 2000, issued to Haake et al;

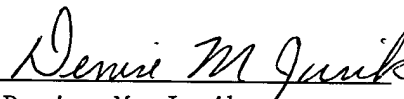
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Dated: 10/24/2003

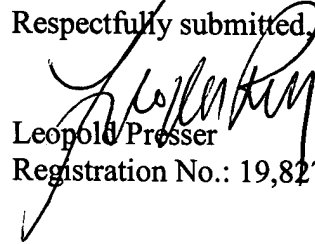
  
Denise M. Jurik

3. U.S. Patent No. 6,125,043, dated September 26, 2000, issued to Hauer et al;
4. U.S. Patent No. 5,857,049, dated January 5, 1999, issued to Beranek et al;
5. U.S. Patent No. 5,661,831, dated August 26, 1997 issued to Sasaki et al;
6. U.S. Patent No. 5,631,988, dated May 20, 1997 issued to Swirhun et al;
7. U.S. Patent No. 5,449,312, dated March 12, 1996 issued to Hahn et al;
8. U.S. Patent No. 5,420,9540, dated May 30, 1995 issued to Swirhun et al;
9. U.S. Patent No. 4,892,337, dated January 9, 1990 issued to Randle;
11. European Patent Appln. No. EP 1 120 673 A1, dated August 1, 2001;
12. Japanese Publication No. JP2000056189A, dated February 25, 2000 together with English-language Abstract;
13. Japanese Publication No. JP2000022279A, dated January 21, 2000 together with English-language Abstract;
14. Japanese Publication No. JP8179154A, dated June 26, 1996, together with English-language Abstract; and
15. Japanese Publication No. JP1140104A, dated February 22, 1989, together with English-language Abstract;

Applicants are submitting a copies of the above-cited references.

Inasmuch as this Information Disclosure Statement is being submitted in accordance with the schedule set out in 37 C.F.R. § 1.97(b), no statement or fee is required.

Respectfully submitted,

  
Leopold Presser  
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# **LIST OF PRIOR ART CITED BY APPLICANT**

(Use several sheets if necessary)

**Atty. Docket No.**  
END920010133US1 (15171)

**Serial N .**  
Unassigned

**Applicant**  
Eric A. Johnson, et al.

**Filing Date**  
Herewith

**Group**  
Unassigned

## **U.S. PATENT DOCUMENTS**

EXAMINER INITIAL*		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (if appropriate)
	AA	6,222,967 B1	4/24/01	Amano, et al.			
	AB	6,164,837	12/26/00	Haake, et al.			
	AC	6,125,043	9/26/00	Hauer, et al.			
	AD	5,857,049	1/5/99	Beranek, et al.			
	AE	5,661,831	8/26/97	Sasaki, et al.			
	AF	5,631,988	5/20/97	Swirhun, et al.			
	AG	5,449,312	3/12/96	Hahn, et al.			
	AH	5,420,954	5/30/95	Swirhun, et al.			
	AI	4,892,337	1/9/90	Randle			

		Foreign Document Number	Date	Country	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
		EP 1 120 673 A1	8/1/01	EPO				
		JP2000056189A	2/25/00	JAPAN				
		JP2000022279A	1/21/00	JAPAN				
		JP8179154A	6/26/96	JAPAN				
		JP1140104A	2/22/89	JAPAN				

## **OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)**


**EXAMINER**

**DATE CONSIDERED**

\* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.